



CON2 - Switch input for Clock Select
 CON3(1) - CK_NPUR -> Reset line from MON board
 CON3(4) - FCLK -> Clock select line from MON board
 CON3(2) - MCLK -> Manual clock line from MON board
 CON4(1) - CLK1 -> Clock output 1
 CON4(3) - CLK2 -> Clock output 2

Title			AGC Replica - Clock Generator		
Size	Number	Revision			
A3		REV 2.0			
Date:	26/01/2008	Sheet 1 of 1		Drawn By: Philip Schmidt	
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